

# RJK2508DPK

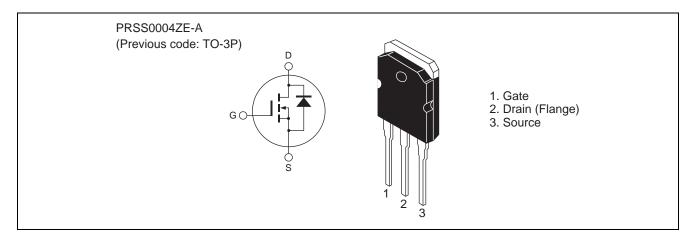
# Silicon N Channel MOS FET High Speed Power Switching

REJ03G0508-0300 Rev.3.00 Jun 30, 2010

#### **Features**

- Low on-resistance
- Low leakage current
- High speed switching

#### **Outline**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to Source voltage	V <sub>DSS</sub>	250	V
Gate to Source voltage	V <sub>GSS</sub>	±30	V
Drain current	I <sub>D</sub>	50	А
Drain peak current	I <sub>D (pulse)</sub> Note1	100	А
Body-Drain diode reverse Drain current	I <sub>DR</sub>	50	А
Body-Drain diode reverse Drain peak current	I <sub>DR (pulse)</sub> Note1	100	А
Avalanche current	I <sub>AP</sub> Note3	17	А
Avalanche energy	E <sub>AR</sub> Note3	18.0	mJ
Channel dissipation	Pch Note2	150	W
Channel to case thermal impedance	θch-c	0.833	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tc = 25°C
- 3. STch =  $25^{\circ}$ C, Tch  $\leq 150^{\circ}$ C

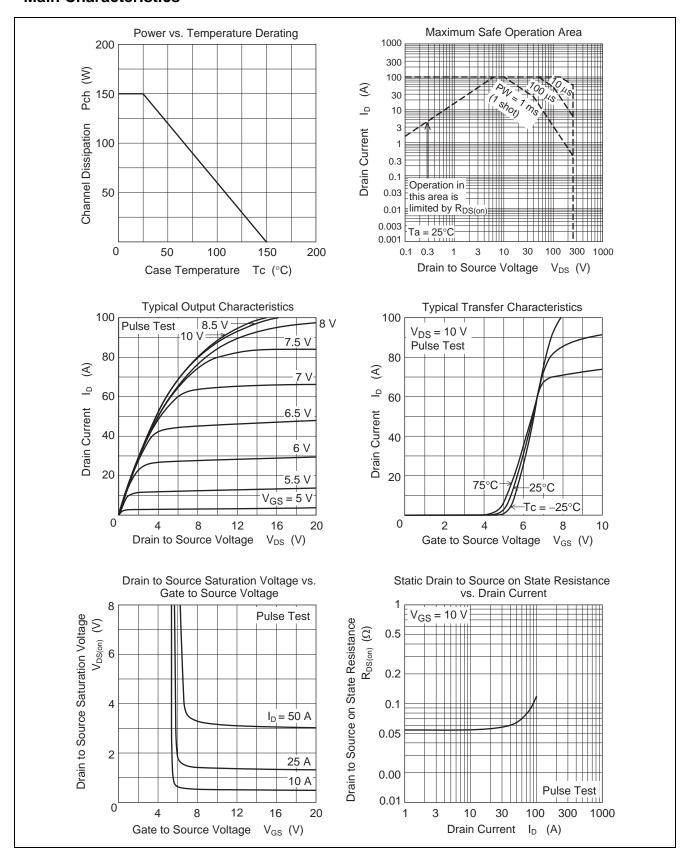
# **Electrical Characteristics**

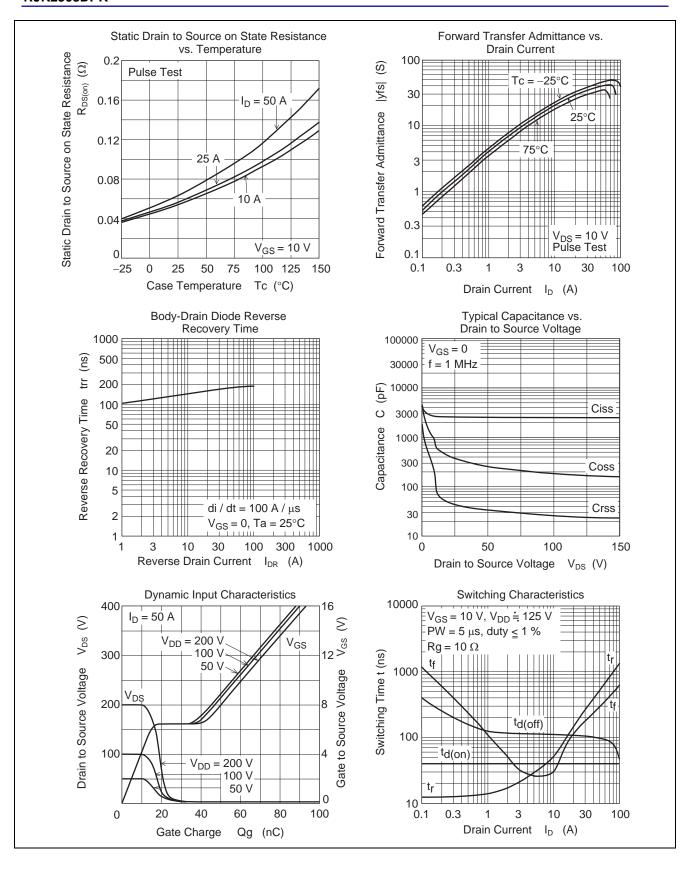
 $(Ta = 25^{\circ}C)$ 

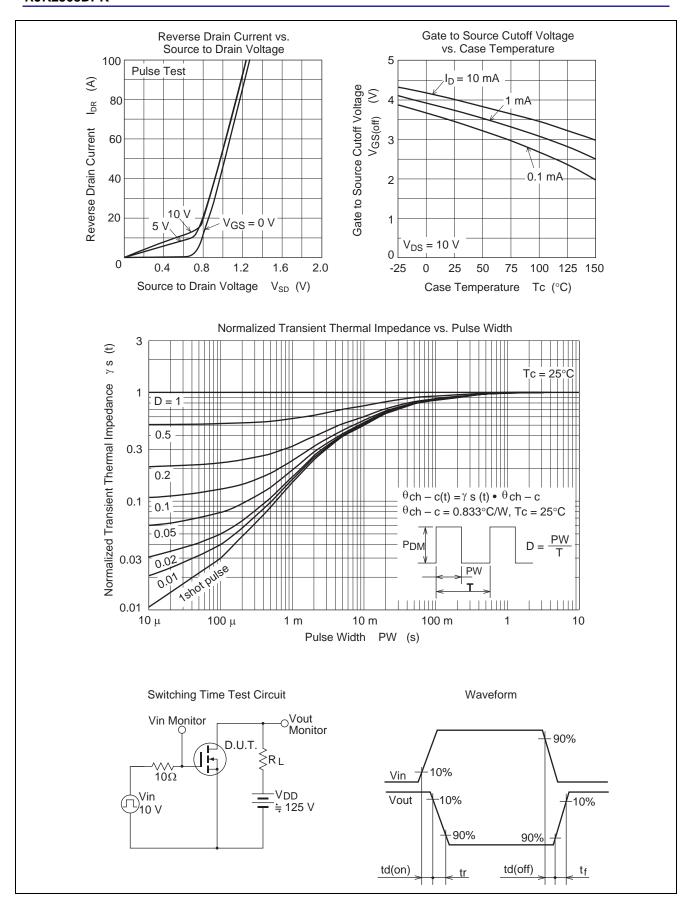
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to Source breakdown voltage	$V_{(BR)DSS}$	250	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero Gate voltage Drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 250 \text{ V}, V_{GS} = 0$
Gate to Source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to Source cutoff voltage	$V_{GS(off)}$	3.0	_	4.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Forward transfer admittance	yfs	19	32	_	S	$I_D = 25 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Static Drain to Source on state	R <sub>DS(on)</sub>	_	0.056	0.064	Ω	$I_D = 25 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance						
Input capacitance	Ciss		2600	_	pF	V <sub>DS</sub> = 25 V
Output capacitance	Coss		370	_	pF	V <sub>GS</sub> = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	43	_	pF	
Turn-on delay time	t <sub>d(on)</sub>	_	40	_	ns	I <sub>D</sub> = 25 A
Rise time	t <sub>r</sub>	_	210	_	ns	V <sub>GS</sub> = 10 V
Turn-off delay time	$t_{d(off)}$	_	110	_	ns	$R_{L} = 5 \Omega$ $Rg = 10 \Omega$
Fall time	t <sub>f</sub>	_	145	_	ns	
Total Gate charge	Qg	_	60	_	nC	V <sub>DD</sub> = 200 V
Gate to Source charge	Qgs	_	15	_	nC	V <sub>GS</sub> = 10 V I <sub>D</sub> = 50 A
Gate to Drain charge	Qgd	_	26	_	nC	
Body-Drain diode forward voltage	$V_{DF}$	_	1.0	1.5	V	$I_F = 50 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-Drain diode reverse recovery time	trr	_	180	_	ns	$I_F = 50 \text{ A}, V_{GS} = 0$
Body-Drain diode reverse recovery	Qrr	_	1.2	_	μС	diF/dt = 100 A/μs
charge						

Notes: 4. Pulse test

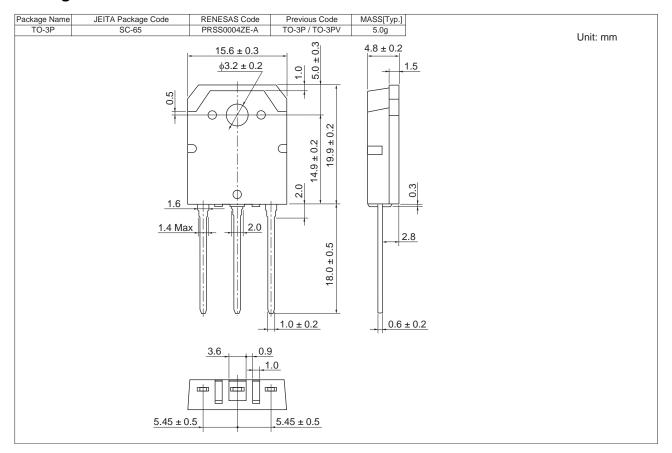
#### **Main Characteristics**







#### **Package Dimensions**



## **Ordering Information**

Part Name	Quantity	Shipping Container
RJK2508DPK-E	30 pcs	Plastic magazine

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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Renesas Electronics America Inc. 2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

เพลายอย อเชียงเทเชง **ทยายู nong Limited** Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2866-9318, Fax: +852-2866-9022/9044

Renesas Electronics Taiwan Co., Ltd.

7F, No. 363 Fu Shing North Road Taipei, Taiwar Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.

1 harbourFront Avenue, #06-10, keppel Bay Tower, Singapore 098632
Tel: +65-6273-0200, Fax: +65-6278-8019
Renesas Electronics Malaysia Sdn.Bhd.

เพราะสอน เมราะเพราะเพราะสามารถ งสท.**ษกด.** Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd. 11F., Samik Lavied' or Bldg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea Tel: 482-2-588-3737, Fax: 482-2-588-5141

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