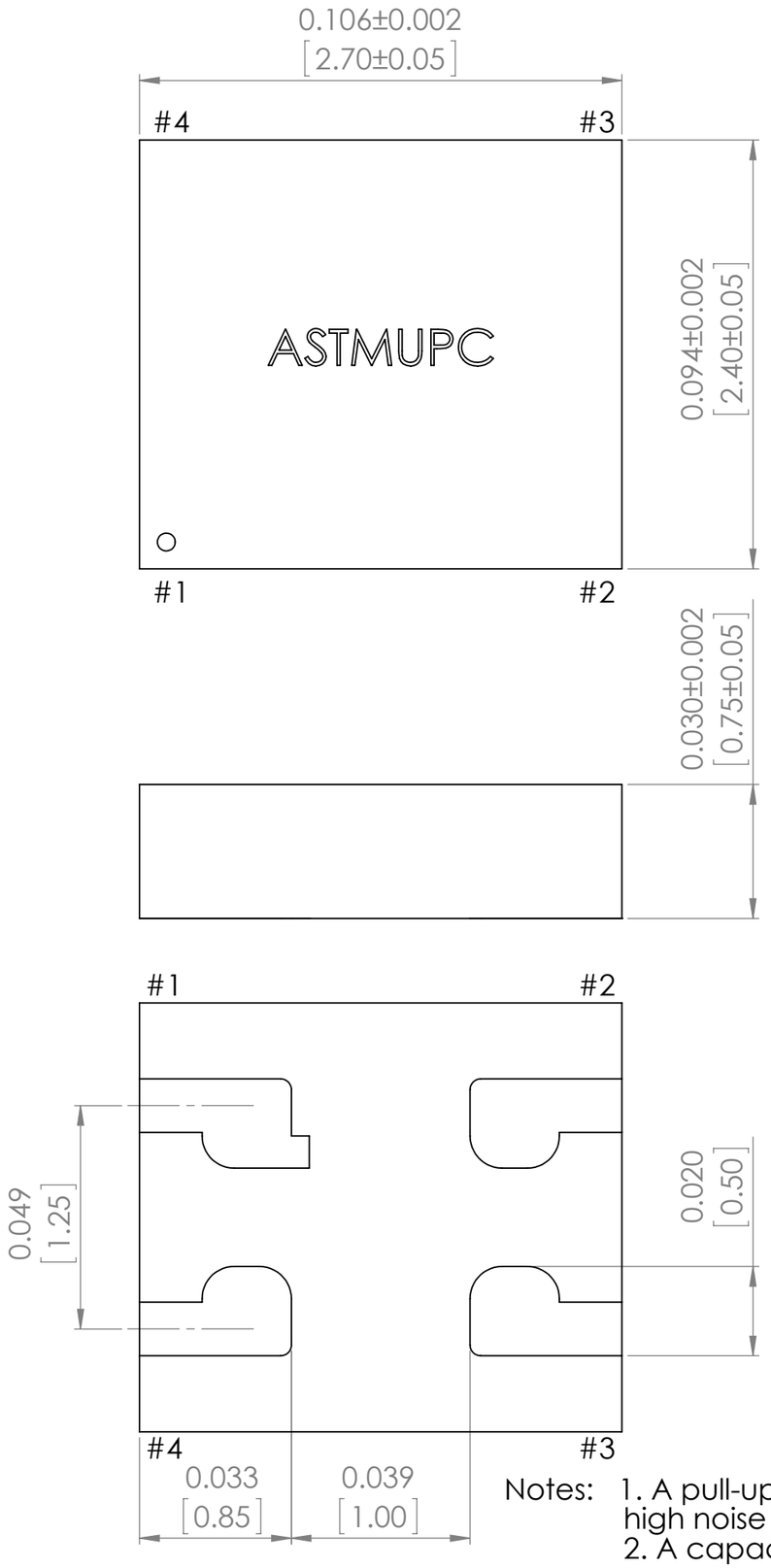
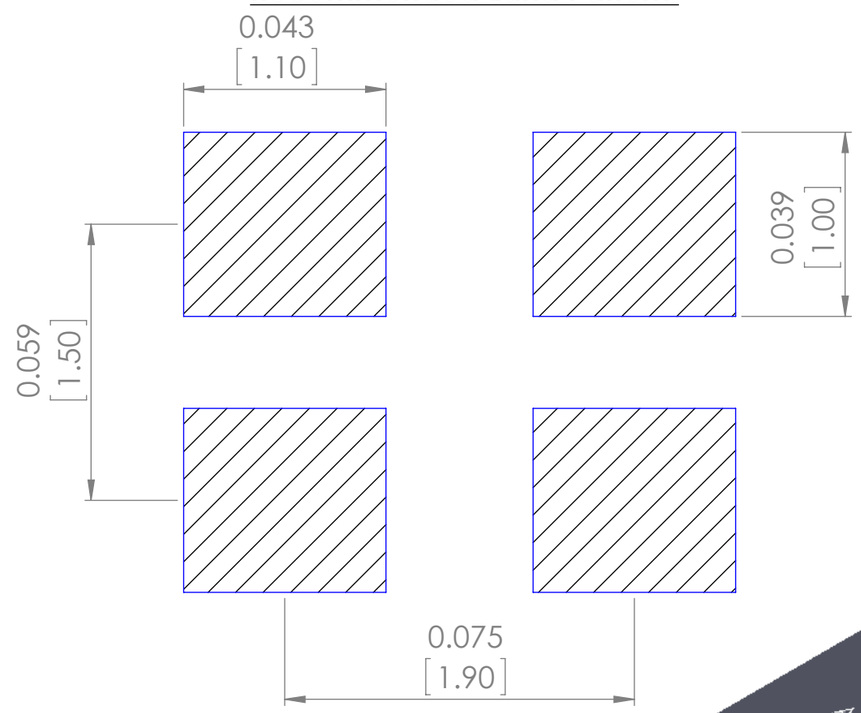
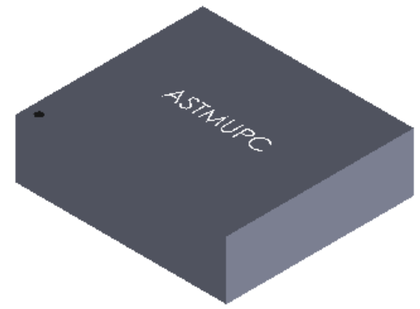
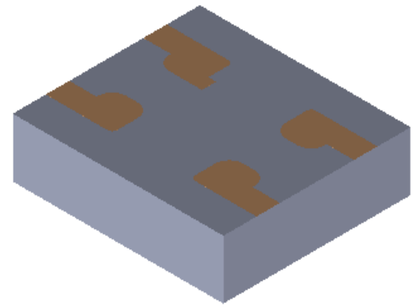
2.7 x 2.4mm Package (Option "D", compatible with 2.5 x 2.0mm footprint)



Recommended Land Pattern







Pin	Name		Functionality
1	OE/$\overline{ST}$	Output Enable	H or Open (1): specified frequency output L: output is high impedance. Only output driver is disabled
		Standby	H or Open (1): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
2	GND	Power	Electrical ground (2)
3	Out	Output	Oscillator clock output
4	V_{dd}	Power	Power supply voltage (2)

TOP PACKAGE MARKING IS FOR ILLUSTRATION PURPOSES ONLY

Notes: 1. A pull-up resistor of <10kΩ between OE/ST pin and Vdd is recommended in high noise environment.
2. A capacitor value of 0.1µF between Vdd and GND is recommended.

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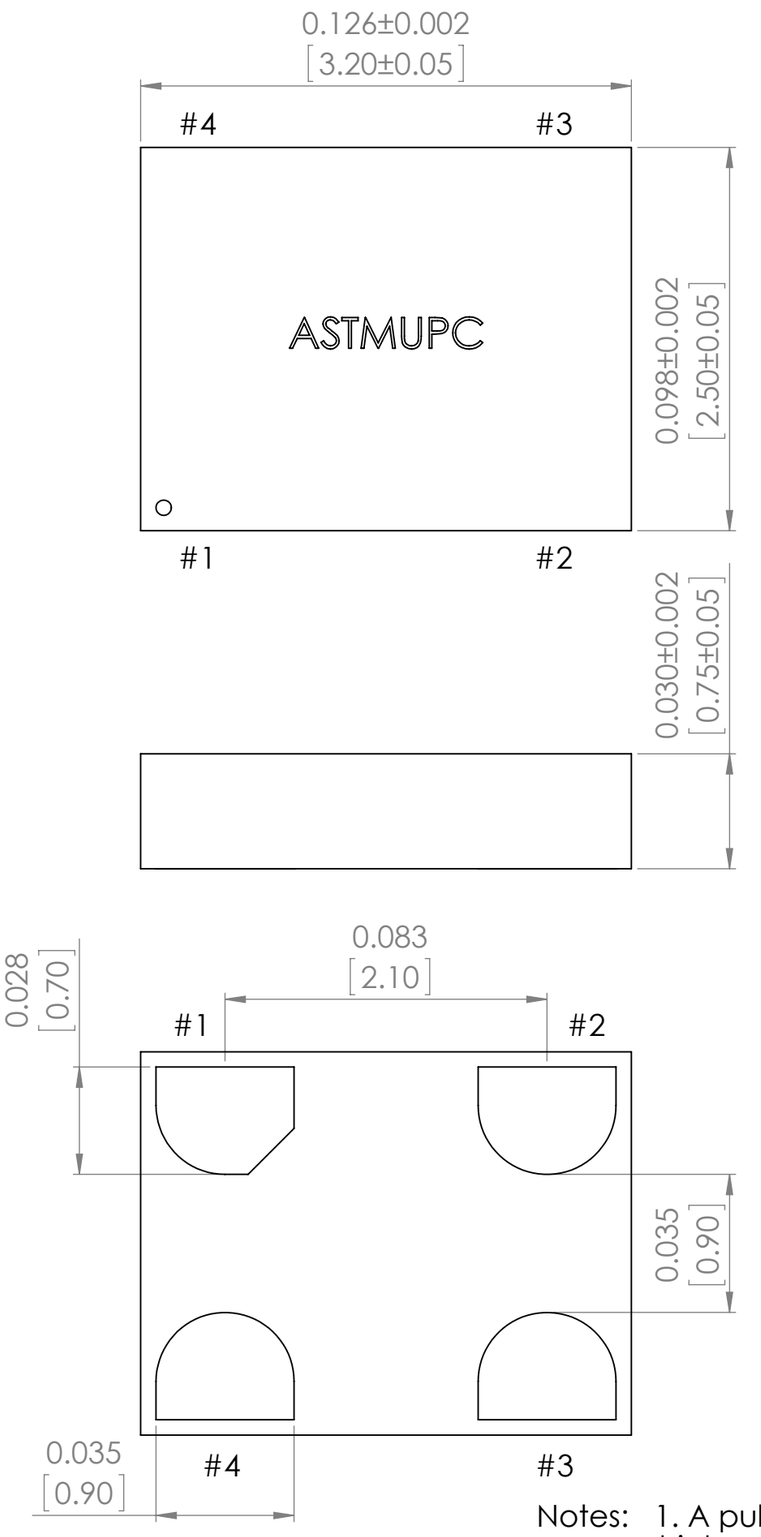
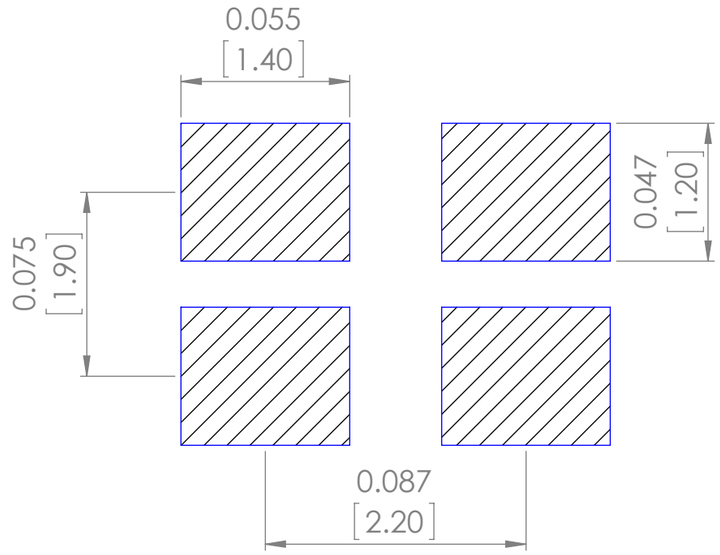
Apracon LLC. 2 Faraday, Suite#B Irvine, Ca. 92618

TITLE:	High Performance, Programmable LVCMOS SMD MEMS Oscillator
DWG NO.	ASTMUPC
	A3
SCALE: 30:1	SHEET 1 OF 4

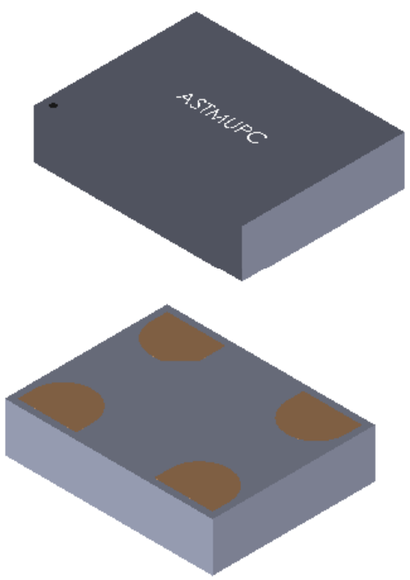
3.2 x 2.5mm Package (Option "E")



Recommended Land Pattern



Pin	Name		Functionality
1	OE/$\overline{ST}$	Output Enable	H or Open (1): specified frequency output L: output is high impedance. Only output driver is disabled
		Standby	H or Open (1): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
2	GND	Power	Electrical ground (2)
3	Out	Output	Oscillator clock output
4	Vdd	Power	Power supply voltage (2)



TOP PACKAGE MARKING IS FOR ILLUSTRATION PURPOSES ONLY

Notes: 1. A pull-up resistor of <10kΩ between OE/ST pin and Vdd is recommended in high noise environment.
2. A capacitor value of 0.1µF between Vdd and GND is recommended.

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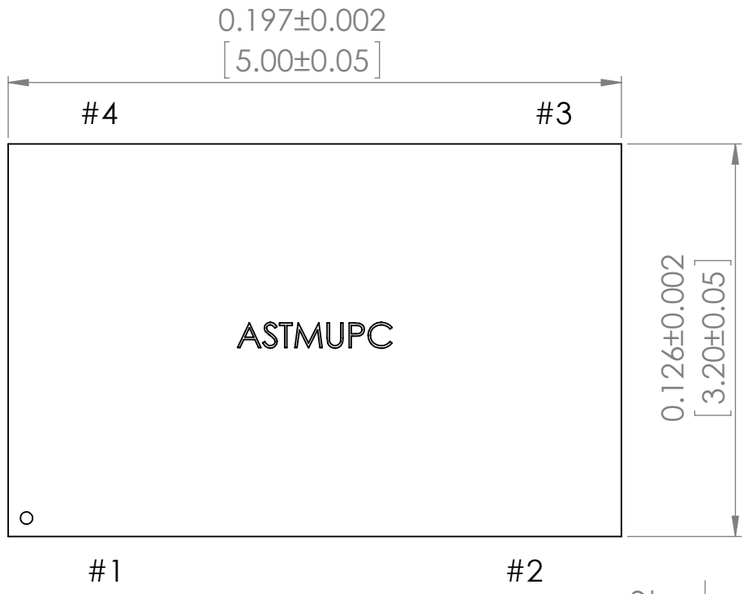
TITLE: High Performance, Programmable LVCMOS SMD MEMS Oscillator

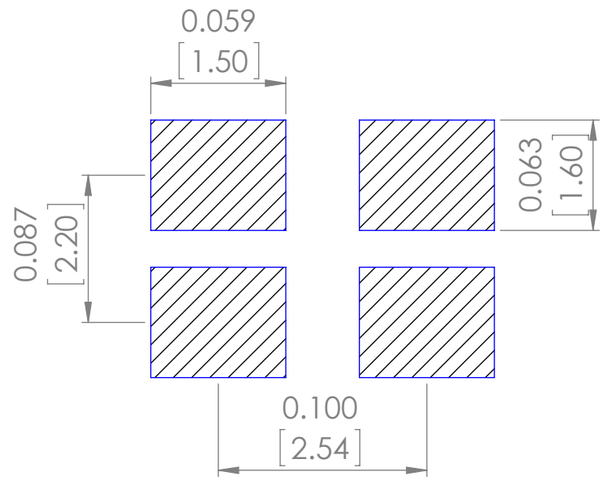
DWG NO. ASTMUPC | A3

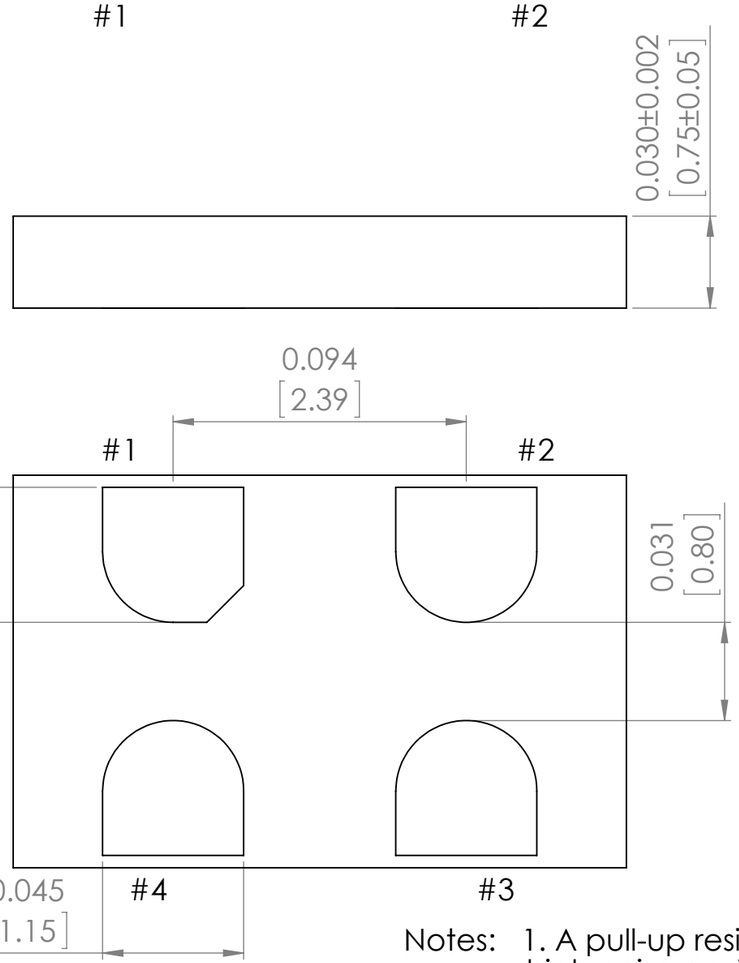
SCALE: 25:1 | SHEET 2 OF 4

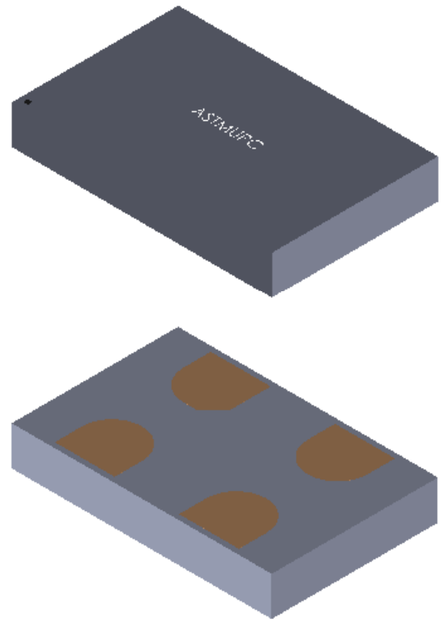
5.0 x 3.2mm Package (Option “FL”)



Recommended Land Pattern







Pin	Name		Functionality
1	OE/$\overline{ST}$	Output Enable	H or Open (1): specified frequency output L: output is high impedance. Only output driver is disabled
		Standby	H or Open (1): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
2	GND	Power	Electrical ground (2)
3	Out	Output	Oscillator clock output
4	Vdd	Power	Power supply voltage (2)

TOP PACKAGE MARKING IS FOR ILLUSTRATION PURPOSES ONLY

Notes: 1. A pull-up resistor of <10kΩ between OE/$\overline{ST}$ pin and Vdd is recommended in high noise environment.
2. A capacitor value of 0.1µF between Vdd and GND is recommended.

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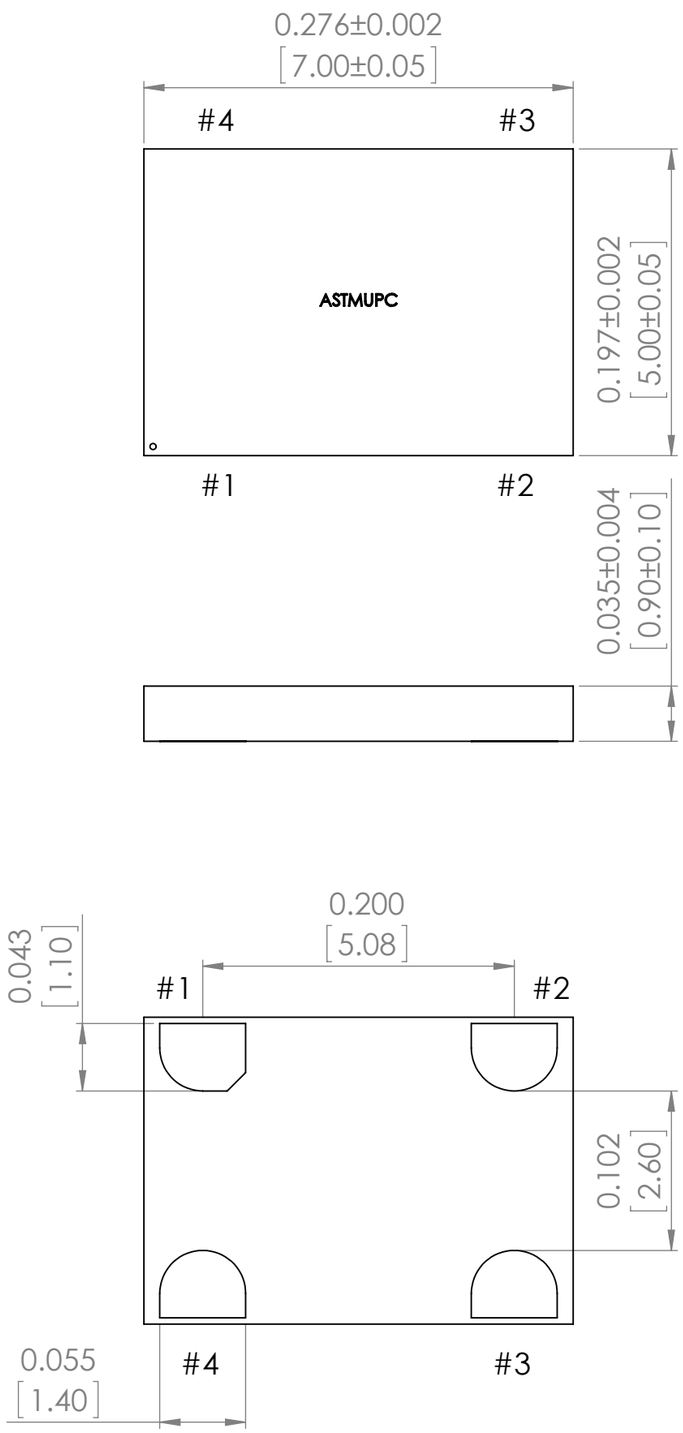
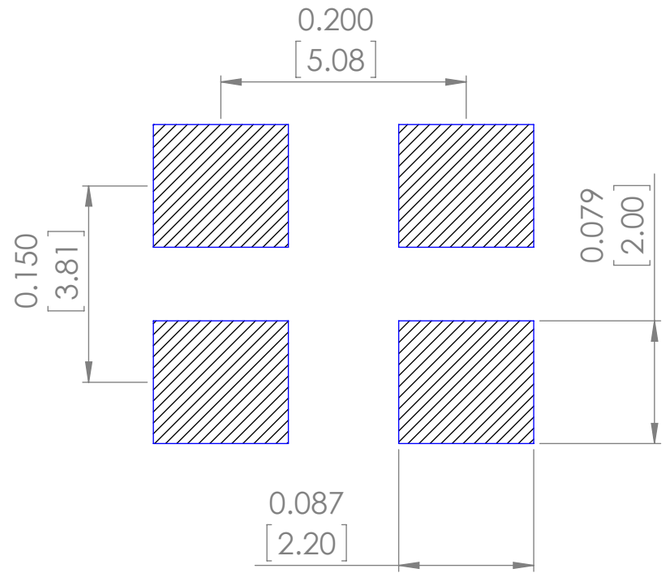
TITLE:	High Performance, Programmable LVCMOS SMD MEMS Oscillator
DWG NO.	ASTMUPC
	A3
SCALE:20:1	SHEET 3 OF 4

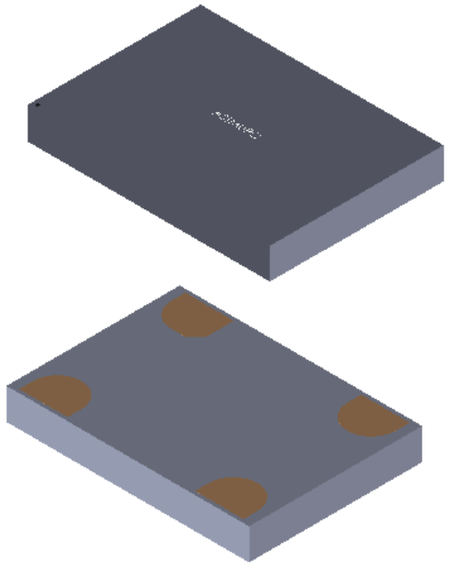
7.0 x 5.0mm Package (Option "V")



Recommended Land Pattern



Pin	Name		Functionality
1	OE/$\overline{ST}$	Output Enable	H or Open (1): specified frequency output L: output is high impedance. Only output driver is disabled
		Standby	H or Open (1): specified frequency output L: output is low (weak pull down). Device goes to sleep mode. Supply current reduces to Istd.
2	GND	Power	Electrical ground (2)
3	Out	Output	Oscillator clock output
4	Vdd	Power	Power supply voltage (2)



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Notes: 1. A pull-up resistor of <10kΩ between OE/$\overline{ST}$ pin and Vdd is recommended in high noise environment.
2. A capacitor value of 0.1µF between Vdd and GND is recommended.

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TITLE: High Performance, Programmable LVCMOS SMD MEMS Oscillator

DWG NO. ASTMUPC | A3

SCALE:10:1 | SHEET 4 OF 4